

図 1

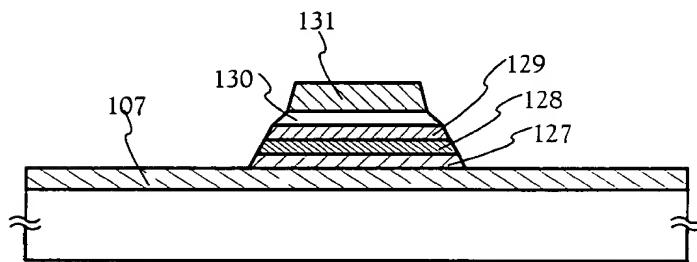


図 2

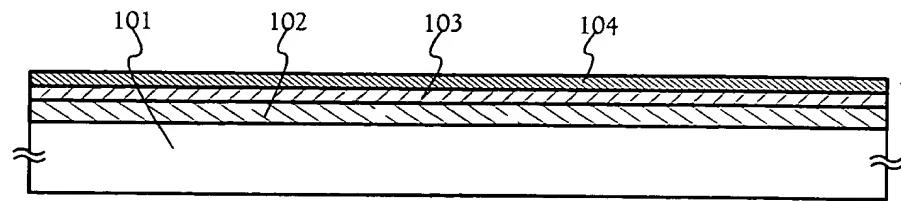


図 3 A

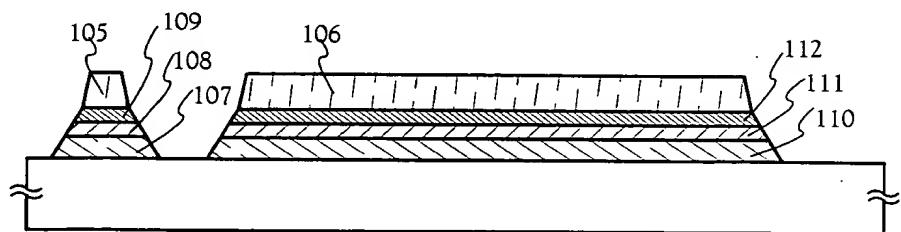


図 3 B

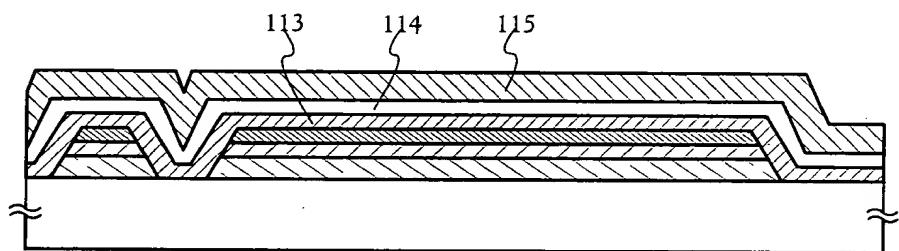


図 3 C

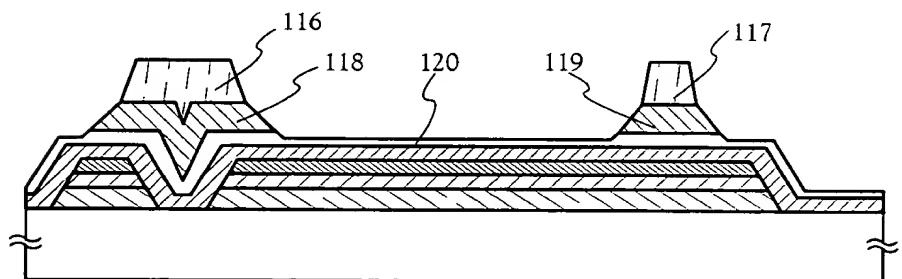


図 3 D

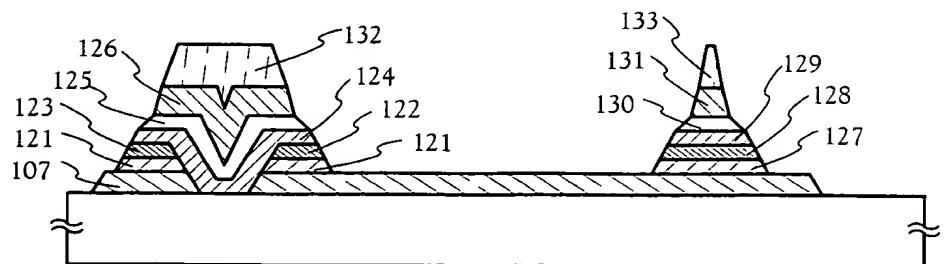


図 3 E

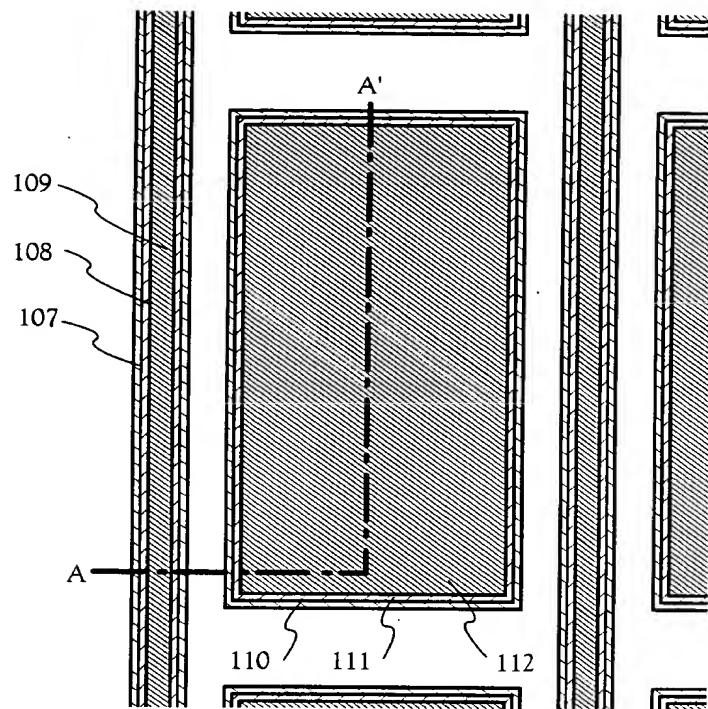


図 4 A

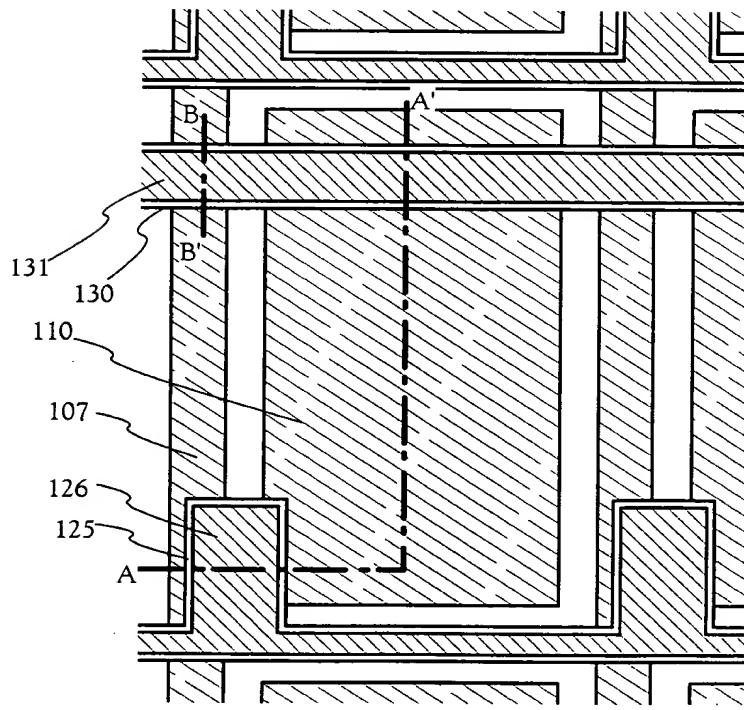


図 4 B

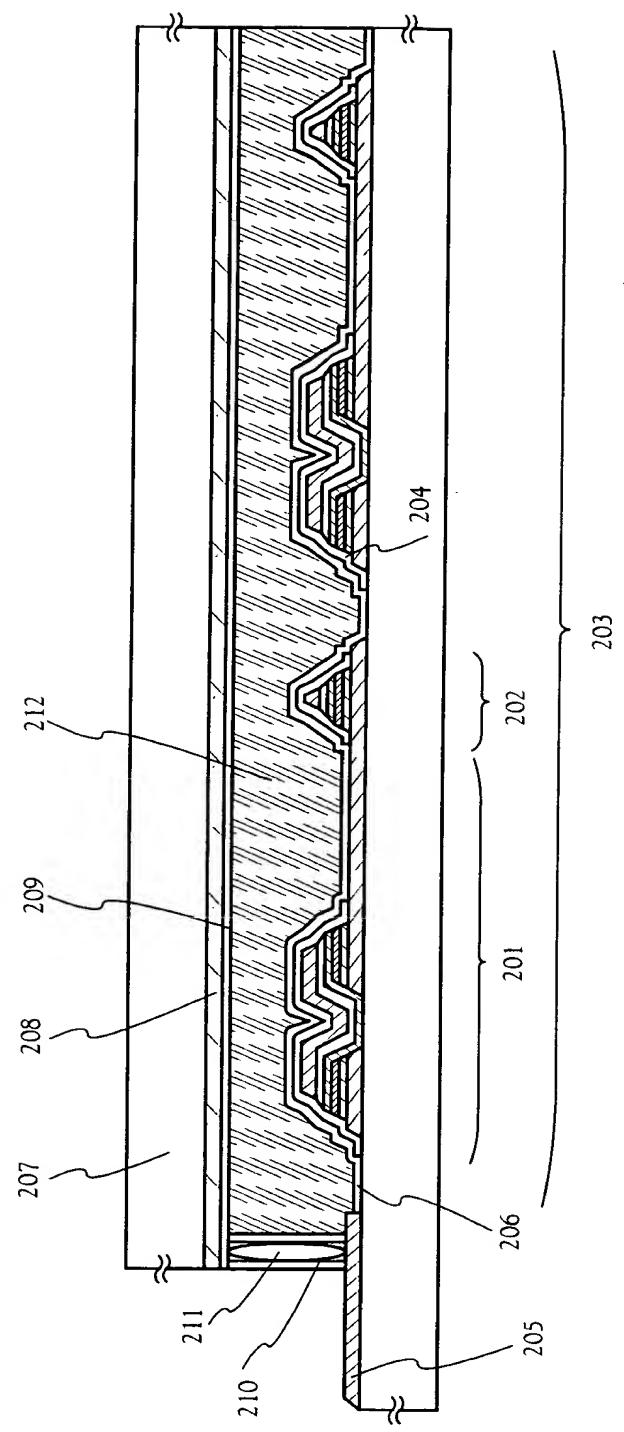


図5

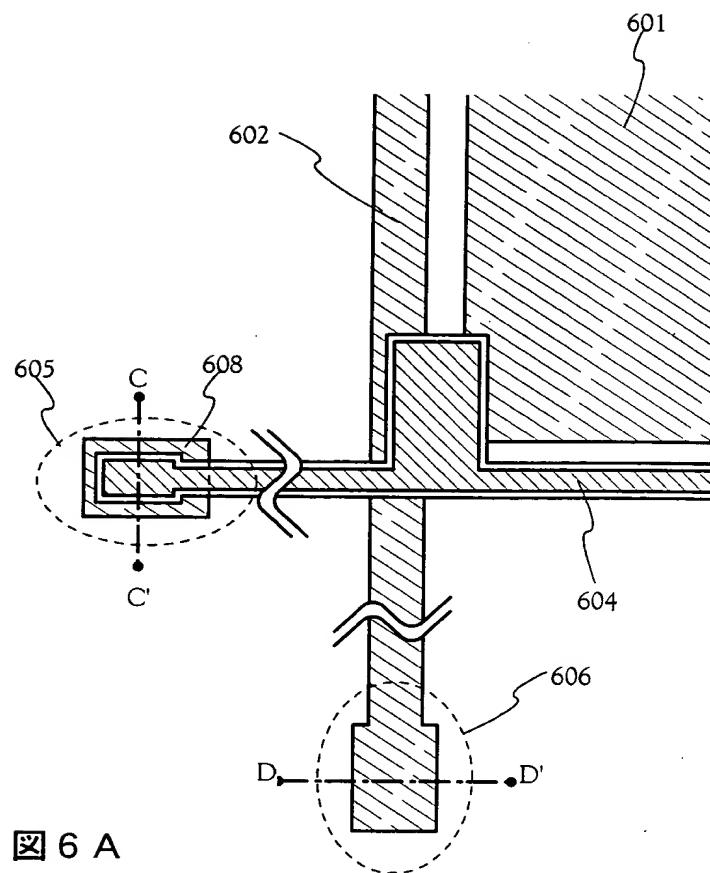


図 6 A

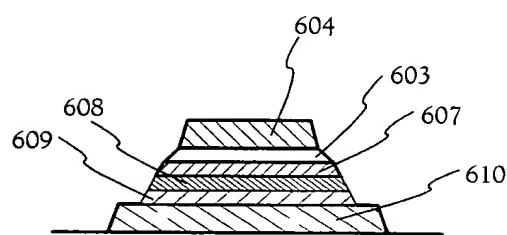


図 6 B

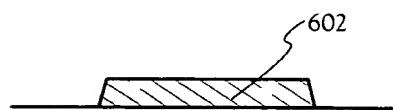


図 6 C

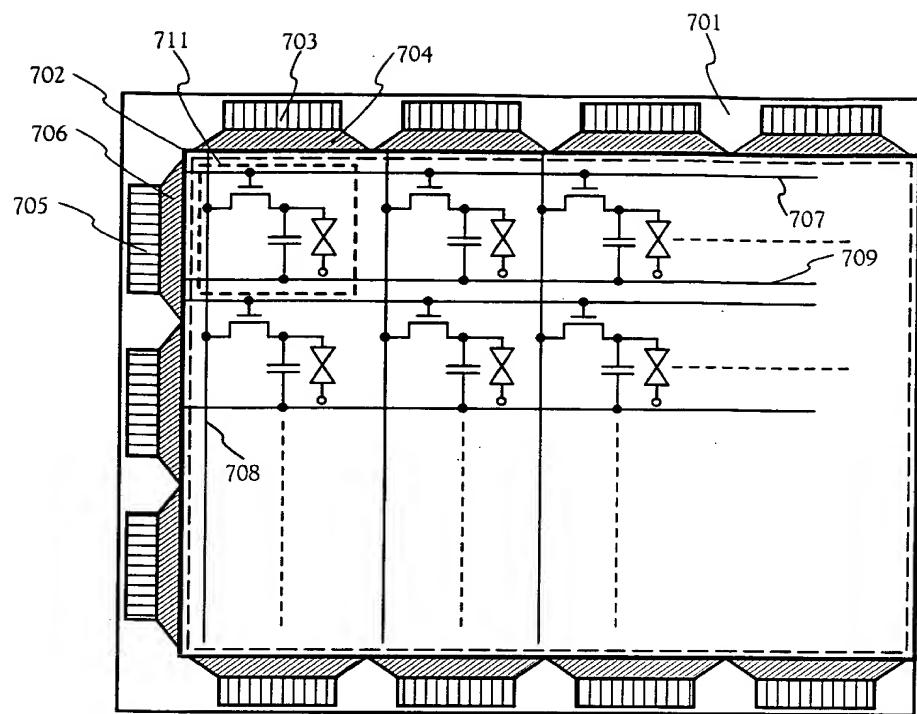


図 7 A

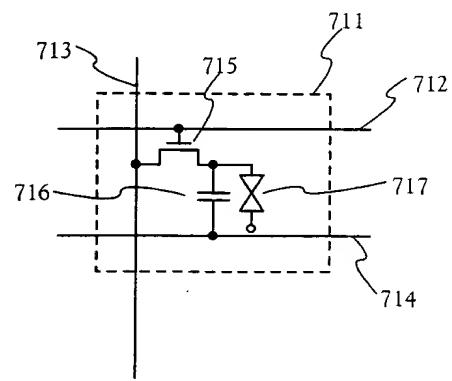


図 7 B

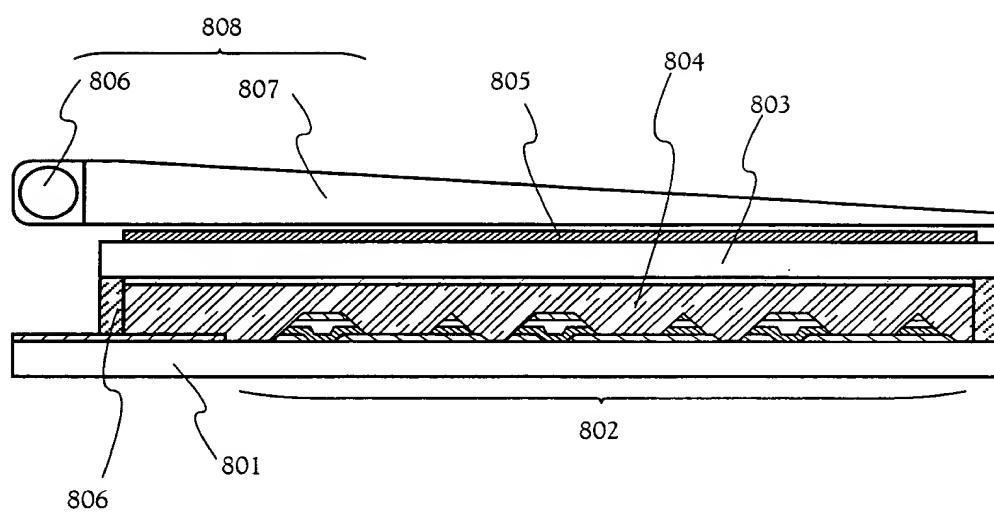


図 8

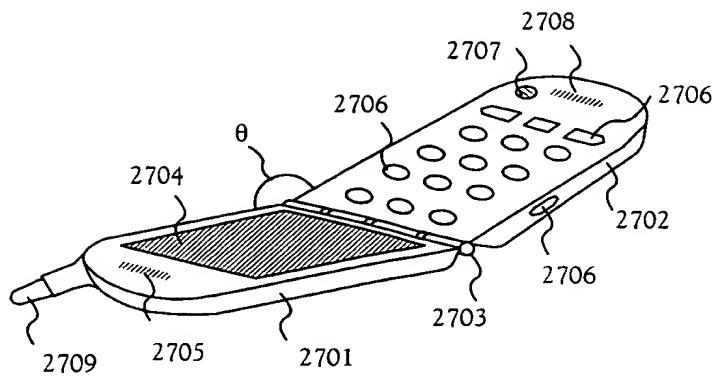


図 9 A

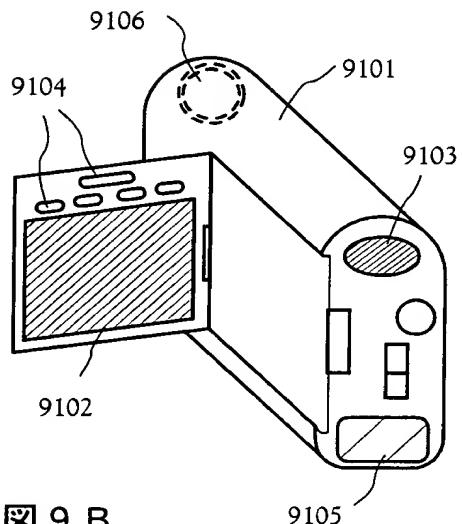


図 9 B

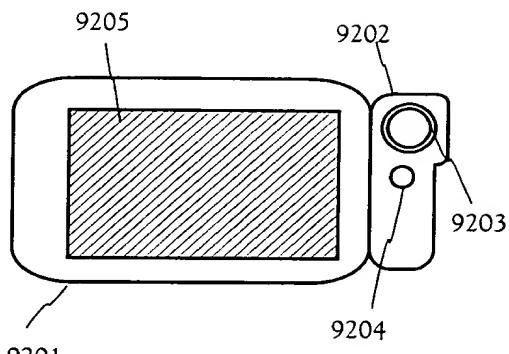


図 9 C

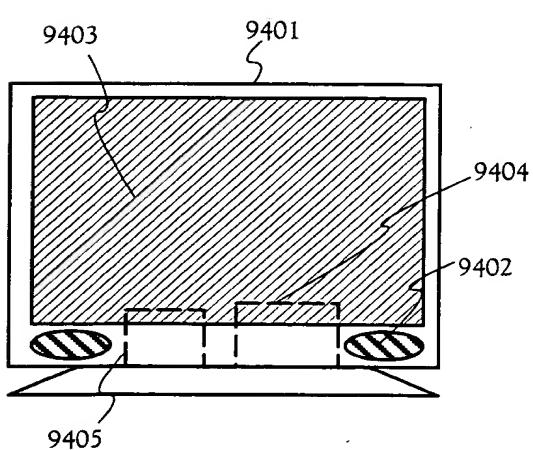


図 9 D

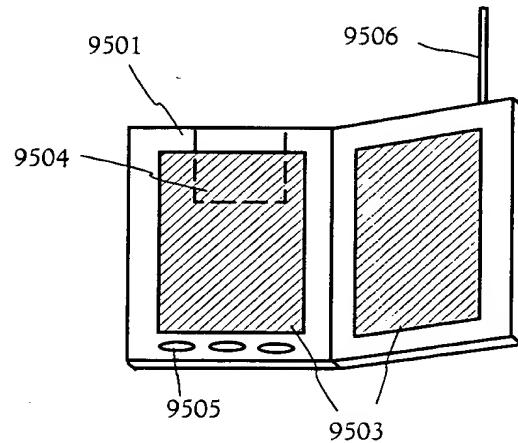


図 9 E

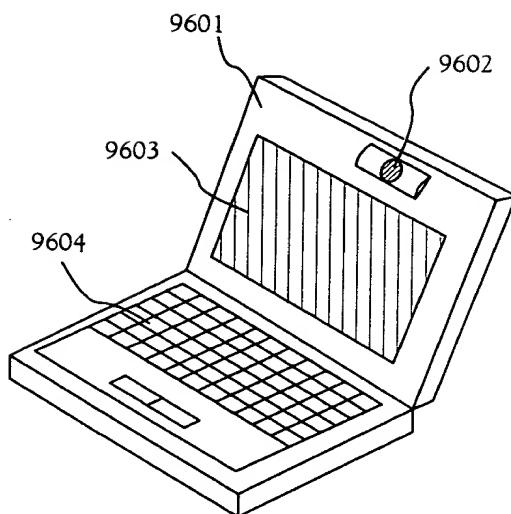


図 10 A

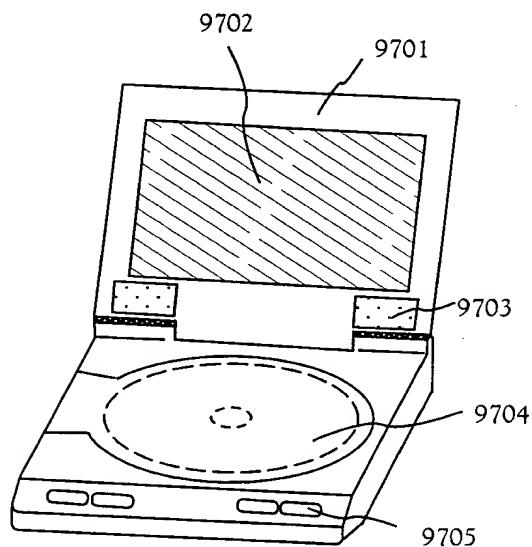


図 10 B

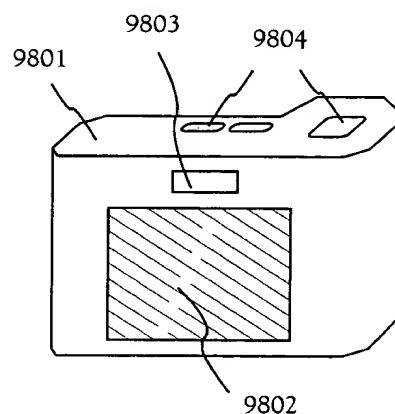


図 10 C